

Embedded core processors are becoming a vital part of today's system-on-a-chip in the growing areas of telecommunications, multimedia and consumer electronics. This is mainly in response to a need to track evolving standards with the flexibility of embedded software. Consequently, maintaining the high product performance and low product cost requires a careful design of the processor tuned to the application domain. With the increased presence of instruction-set processors, retargetable software compilation techniques are critical, not only for improving engineering productivity, but to allow designers to explore the architectural possibilities for the application domain. *Retargetable Compilers for Embedded Core Processors*, with a Foreword written by Ahmed Jerraya and Pierre Paulin, overviews the techniques of modern retargetable compilers and shows the application of practical techniques to embedded instruction-set processors. The methods are highlighted with examples from industry processors used in products for multimedia, telecommunications, and consumer electronics. An emphasis is given to the methodology and experience gained in applying two different retargetable compiler approaches in industrial settings. The book also discusses many pragmatic areas such as language support, source code abstraction levels, validation strategies, and source-level debugging. In addition, new compiler techniques are described which support address generation for DSP architecture trends. The contribution is an address calculation transformation based on an architectural model. *Retargetable Compilers for Embedded Core Processors* will be of interest to embedded system designers and programmers, the developers of electronic design automation (EDA) tools for embedded systems, and researchers in hardware/software co-design.

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**: Clifford Liem: Books, Biography, Blog, Audiobooks** *Retargetable Compilers for Embedded Core Processors* Core Processors Book Subtitle: Methods and Experiences in Industrial Applications Pages: pp 13-46 **Tools for Instruction-Set Design and Redesign - Springer** : *Retargetable Compilers for Embedded Core Processors: Methods and Experiences in Industrial Applications*: Clifford Liem: ?? **Retargetable compilers and tools for embedded processors in** *Retargetable Compilers for Embedded Core Processors: Methods and Experiences in Industrial Applications*. Embedded core processors are becoming a vital **Retargetable Compilers for Embedded Core Processors: Methods** *Retargetable Compilers for Embedded Core Processors* will be of interest to embedded **Core Processors: Methods and Experiences in Industrial Applications. Practical Issues in Compiler Design for Embedded Processors** 2010? 12? 2? *Retargetable Compilers for Embedded Core Processors: Methods and Experiences in Industrial Applications*, Clifford Liem, Not Avail. **Two Emerging Approaches: Model-based and Rule-driven - Springer** Mar 9, 2013 *Retargetable Compilers for Embedded Core Processors* will be of **Core Processors: Methods and Experiences in Industrial Applications. Pushing the Capabilities of Compiler Methodologies in Industry** *Retargetable Compilers for Embedded Core Processors* for embedded processors developed and used by the author and colleagues in industrial contexts. **Retargetable Compilers for Embedded Core Processors: Methods** Embedded core processors are becoming a vital part of today's system-on-a-chip in *Methods and Experiences in Industrial Applications* overviews the techniques of modern retargetable compilers and shows the application of The methods are highlighted with examples from industry processors

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